



#5/Election
2/11/03
VA

XA-9630
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tsuyoshi FUJIWARA

Appln. No.: 10/083,416

Group Art Unit: 2811

Filed: February 27, 2002

Examiner: C. Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

RESPONSE

Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

FEB-9 2003

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Sir:

In response to the Office Action mailed January 16, 2003, and to the requirement for restriction therein, Applicants elect Group II (Claims 1-16), reserving the right to file a divisional application regarding the non-elected invention.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16 and 1.17 which may be required by this paper, and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and

has not been requested separately, then such extension is
hereby requested.

Respectfully submitted,

MWS:jab

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By: *Mitchell W. Shapiro*
Mitchell W. Shapiro
Reg. No. 31,568

February 5, 2003

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